TS PACKET **TS PACKET TS3** _ TS3 OUTPUT t1 INPUT t1 TS PACKET TS PACKET **TS2** √ TS2 TS PACKET TS PACKET TS1 **OUTPUT COUNTER 42** INPUT COUNTER 41 Fig. 4C Fig. 4D Fig. 4A Fig. 4B

TS PACKET TS PACKET _ TS3 **TS3** OUTPUT TMS INPUT TMS **TS PACKET** TS PACKET TS2) TS2 **TS PACKET** TS PACKET TS1 TS1 **OUTPUT COUNTER 42 INPUT COUNTER 41** Fig. 6C Fig. 6D Fig. 6A Fig. 6B